

## High Voltage Interface/Driver ICs

### Sink Only Outputs - Open Drain N-Channel

Device	Output Channels	Direction	Logic Configuration	Output		Package Options
				Operating Voltage (V)	Current per Channel (mA)	
<a href="#">HV5122</a>	32	CCW	Serial to parallel converter with output enable and strobe	225	100	44-Lead PQFP (PG)
						44-Lead PLCC (PJ)
<a href="#">HV5222</a>	32	CW	Serial to parallel converter with output enable and strobe	225	100	44-Lead PQFP (PG)
						44-Lead PLCC (PJ)
<a href="#">HV5522</a>	32	CCW	Serial to parallel converter with latches, polarity, and blanking	220	100	44-Lead PQFP (PG)
						44-Lead PLCC (PJ)
<a href="#">HV5523</a>	32	CCW	Serial to parallel converter with latches, polarity, and blanking	220	100	44-Lead QFN (K7)
<a href="#">HV5530</a>	32	CCW	Serial to parallel converter with latches, polarity, and blanking	300	100	44-Lead PQFP (PG)
						44-Lead PLCC (PJ)
<a href="#">HV5622</a>	32	CW	Serial to parallel converter with latches, polarity, and blanking	220	100	44-Lead PQFP (PG)
						44-Lead PLCC (PJ)
<a href="#">HV5623</a>	32	CW	Serial to parallel converter with latches, polarity, and blanking	220	100	44-Lead QFN (K7)
<a href="#">HV5630</a>	32	CW	Serial to parallel converter with latches, polarity, and blanking	300	100	44-Lead PLCC (PJ)

### Sink Only Outputs - Open Drain P-Channel

Device	Output Channels	Direction	Logic Configuration	Output		Package
				Operating Voltage (V)	Current per Channel (mA)	
<a href="#">HV4522</a>	32	CCW	Serial to parallel converter with latches, polarity, and blanking	-220	-60	44-Lead PLCC (PJ)
<a href="#">HV4622</a>	32	CW	Serial to parallel converter with latches, polarity, and blanking	-220	-60	44-Lead PLCC (PJ)
<a href="#">HV57009</a>	64	B	Current controlled driver with latches and blanking, two 32-bit shift registers	-85	Controllable to -2	80-Lead PQFP (PG)

**High Voltage Interface/Driver ICs (cont.)**

**Source-Sink Outputs - Push-Pull**

Device	Out-put Channels	Dir.	Logic Configuration	Out-put Voltage (V)	Output Current per Channel (mA)	Output Structure	Clock Rate max (MHz)	Bits	Out-puts On	Package Options
<a href="#">HV3418</a>	64	Both	Serial to parallel converter with latches, polarity, and blanking	+180	±5.0	Push-Pull	6	1	All	80-Lead PQFP (PG)
<a href="#">HV507</a>	64	Both	Serial to parallel converter with latches, polarity, and blanking	+300	±1.0	Push-Pull	8	1	All	80-Lead PQFP (PG)
<a href="#">HV508</a>	2	-	H-Bridge output with two output voltage level selections and polarity	+45	+225 -270	Push-Pull H-Bridge	-	-	-	8-Lead SOIC (LG)
<a href="#">HV513</a>	8	CW	Serial to parallel converter with latches, polarity, and blanking HI-Z and short circuit detect	+250	±20	Push-Pull	8	1	All	24-Lead SOW (WG)
										32-Lead QFN (K7)
<a href="#">HV514</a>	8	CW	Serial to parallel converter with data latches, channel polarity select, and blanking	+250	±20	Push-Pull	8	1	All	24-Lead SOW (WG)
<a href="#">HV518</a>	32	CW	Serial to parallel converter with latches, enable, and strobe	+80	+1.0 -25	Push-Pull	6	1	All	40-Lead DIP (P)
										44-Lead PLCC (PJ)
<a href="#">HV5308B</a>	32	CW	Serial to parallel converter with latches, and output enable	+80	±20	Push-Pull	8	1	All	44-Lead PQFP (PG)
										44-Lead PLCC (PJ)
<a href="#">HV5408B</a>	32	CCW	Serial to parallel converter with latches, and output enable	+80	±20	Push-Pull	8	1	All	44-Lead PQFP (PG)
										44-Lead PLCC (PJ)
<a href="#">HV574</a>	80	Both	Serial to parallel converter with latches, polarity, and blanking	+80	+15 -30	Push-Pull	25	4	All	100-Lead PQFP (PG)
<a href="#">HV57708</a>	64	Both	EL driver with latches, polarity, and blanking with four 16-bit shift registers	+80	+12 -15	Push-Pull	6	4	All	80-Lead PQFP (PG)
<a href="#">HV57908</a>	64	Both	EL or plasma driver w/ latches, blanking, polarity, and single shift register	+80	+12 -15	Push-Pull	8	1	All	80-Lead PQFP (PG)
<a href="#">HV5812</a>	20	CW	Serial to parallel converter with latches, blanking and strobe	+80	+1.0 -25	Push-Pull	5	1	All	28-Lead DIP (P)
										28-Lead PLCC (PJ)
										28-Lead SOW (WG)
<a href="#">HV583</a>	128	Both	Serial to parallel converter with latches, enable, and blanking	+80V	±20	Push-Pull	40	1	All	Die only

## High Voltage Interface/Driver ICs (cont.)

### Source-Sink Outputs - Push-Pull

Device	Output Channels	Dir.	Logic Configuration	Output Voltage (V)	Output Current per Channel (mA)	Output Structure	Clock Rate max (MHz)	Bits	Outputs On	Package Options
<a href="#"><u>HV632</u></a>	32	Both	Serial to parallel converter with latches, polarity, and blanking	+80	±4.0	Gray Shade PWM	10	8	All	64-Lead PQFP (PG)
<a href="#"><u>HV633</u></a>	32	Both	Amplitude modulated gray shade column driver with 128 output levels	+80	±7.0	Gray Shade Level	12	7	All	64-Lead PQFP (PG)
<a href="#"><u>HV66</u></a>	32	CW	Serial to parallel converter with latches, polarity, and blanking HI-Z and short circuit detect	+60	±5.0	Push-Pull	5	1	All	44-Lead PQFP (PG) 44-Lead PLCC (PJ)
<a href="#"><u>HV6810</u></a>	10	Both	Serial to parallel converter with data latches, channel polarity select, and blanking	+80	+25	Push-Pull	5	1	All	20-Lead PLCC (PJ) 20-Lead SOW (WG)
<a href="#"><u>HV7022C</u></a>	34	Both	Serial to parallel converter with latches, enable, and strobe	+230	±70 (min)	Push-Pull	4	1	One	44-Lead PLCC (PJ)
<a href="#"><u>HV7224</u></a>	40	CW	Serial to parallel converter with latches, and output enable	+240	±70 (min)	Push-Pull	3	1	One	64-Lead PQFP (PG)
<a href="#"><u>HV7620</u></a>	32	Both	Serial to parallel converter with latches, and output enable	+200	±50 (min)	Push-Pull	10	4	All	64-Lead PQFP (PG)
<a href="#"><u>HV9308</u></a>	32	CW	EL or plasma panel driver with latches and output enable	+80	+5.0 -20	Push-Pull	8	1	All	44-Lead PLCC (PJ)
<a href="#"><u>HV9408</u></a>	32	CCW	EL or plasma panel driver with latches and output enable	+80	+5.0 -20	Push-Pull	8	1	All	44-Lead PLCC (PJ)
<a href="#"><u>HV9708</u></a>	32	CW	EL or plasma panel driver with latches, polarity and blanking	+80	+5.0 -20	Push-Pull	8	1	All	44-Lead PLCC (PJ)
<a href="#"><u>HV9808</u></a>	32	CCW	EL or plasma panel driver with latches, polarity and blanking	+80	+5.0 -20	Push-Pull	8	1	All	44-Lead PLCC (PJ)